

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	320	379/433.04.ccls. or 379/433.13.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/03 15:18
L2	23	(dual two) near3 display\$3 and L1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/03 16:11
L3	64	chiang.xp. and 379/433.13.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/03 16:09
L4	1121	345/1.1-1.3.ccls. or 345/2.1-2.3.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/03 16:11
L5	497	(dual two) near3 display\$3 and L4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/03 16:12
L6	1246910	fold\$4 clam flip	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/03 16:12
L7	126	L6 and L5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/03 16:12
S1	1441	455/575.1.ccls. or 455/575.3.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/03 15:09

S2	71041	two near3 display	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/20 19:05
S3	100	S1 and S2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/20 19:24
S4	58057	display near3 (mount fram\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/20 19:25
S5	863	S4 same chip	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/20 19:25
S6	1	S3 and S5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/20 19:25
S7	1	S1 and S5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/20 19:25
S8	141	S5 and S2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/20 19:26
S9	1243314	fold\$4 clam flip	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/20 19:40

S10	45	S8 and S9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/20 19:26
S11	10	("2004/0077385").URPN.	USPAT	OR	OFF	2005/10/20 19:32
S12	5271773	fold\$4 clam flip rotat\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/20 19:40
S13	69	S8 and S12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/20 19:40
S14	58134	display near3 (mount fram\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/25 13:40
S15	863	S14 same chip	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/25 13:30
S16	1244145	fold\$4 clam flip	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/25 13:30
S17	195	S16 and S15	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/25 13:33
S18	1893808	cellular or mobile or cell	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/25 13:36

S19	113	S18 and S17	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/25 13:33
S20	841620	cellular or mobile	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/25 13:36
S21	113	S19 and S17	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/25 13:37
S22	447093	cellular or (mobile near3 (device phone))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/25 13:36
S23	57	S20 and S17	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/25 13:37
S24	50	S22 and S17	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/25 13:37
S25	130981	display near3 (mount fram\$3 hous\$3 cas\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/25 13:40
S26	1748	S25 same chip	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/25 13:40

S27	103	S26 and S16 and S20	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/25 13:54
S28	88657	(dual two) near3 display\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/03 15:10
S29	9423	S28 with (mount fram\$3 hous\$3 cas\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/25 13:56
S30	842	S20 and S29	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/25 13:56
S31	289	S30 and S16	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/25 13:56
S32	833658	pcb or chip	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/25 13:56
S33	93	S32 and S31	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/25 14:20
S34	0	"575""."1".ccls. or "575""."3".ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/25 14:21

S35	1443	455/575.1.ccls. or 455/575.3.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/25 14:21
S36	1636	455/566.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/26 11:27
S37	322	fold\$4 and S36	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/25 14:22
S38	396	S16 and S36	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/25 14:22
S39	109	S38 and S28	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/25 14:22
S40	148473	stagger\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/25 17:08
S41	0	S39 and S40	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/25 17:09
S42	5249	S40 with (circuit chip)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/25 17:10

S43	1	S36 and S42	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/25 18:11
S44	127612	(display\$3 lcd) near3 (fram\$3 hous\$3 mount\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/25 18:12
S45	2857	S35 or S36	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/25 18:12
S46	569	S44 and S45	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/25 18:13
S47	337	S44 and S36	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/25 18:14
S48	319	S46 and S16	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/25 18:14
S49	4	S40 and S48	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/25 18:14
S50	1244145	fold\$4 clam flip	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/26 11:27

S51	1636	455/566.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/26 11:27
S52	396	S50 and S51	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/26 11:51
S53	53326	flex\$4 near5 (lead\$3 tab)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/26 11:28
S54	2	S52 and S53	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/26 11:28
S55	10	"4912602".pn. or "6111760".pn. or "20040203484" or "20040203514" or "20050036605"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/26 12:06
S56	1242	349/58.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/26 12:06
S57	47	S53 and S56	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/26 12:27
S58	107	makino-masami.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/26 12:49

S59	15	sakai-takumi.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/26 21:28
S60	6	"20020142810" or "20040166829" or "20040198476"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/26 12:49
S61	166	(dual near3 (display\$3 LCD)) with (thinner thinning thin small\$2 profile)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/26 21:41
S62	975	efficient\$2 near5 (layout design\$3 position\$3 plac\$3) with (microchip chip)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/26 21:42
S63	22694	(dual two near3 (display\$3 LCD)) with (thinner thinning thin small\$2 profile)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/26 21:41
S64	2949	((dual two) near3 (display\$3 LCD)) with (thinner thinning thin small\$2 profile)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/26 21:42
S65	0	S62 and S64	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/26 21:41
S66	91658	((dual two) near3 (display\$3 LCD))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/26 21:42

S67	5	S66 and S62	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/26 21:42
S68	1008	efficient\$2 near5 (layout design\$3 position\$3 plac\$3 placement) with (microchip chip)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/26 21:43
S69	2337	(efficient\$2 stagger\$3) with (layout design\$3 position\$3 plac\$3 placement) with (microchip chip)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/26 21:47
S70	35	S66 and S69	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/26 21:43
S71	4405	(efficient\$2 stagger\$3 (checker near2 board) (above near3 below) stack\$2) with (layout design\$3 position\$3 plac\$3 placement) with (microchip chip)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/26 21:48
S72	2505	(efficient\$2 stagger\$3 (checker near2 board) (above near3 below) non-stack\$3) with (layout design\$3 position\$3 plac\$3 placement) with (microchip chip)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/26 21:48
S73	38	S72 and S66	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/26 21:49
S74	57	S71 and S66	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/26 21:49

S75	139	fold\$2 with chip near3 mount\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/28 22:17
S76	1	thin near3 phone near3 design	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/28 22:18